Raptor II RapidIO 4.x Switching Development Platform Product Brief

Features

- Supports RapidIO switch development based on Praesum DQU switching IP.

- Stratix 10 MX FPGA with ample resources for both the DQU switch fabric and customer defined processing and bridging logic.

- 1U Rack Mount Chassis.

- Six FMC Line Card Interfaces, supporting up to twenty four front panel switch ports.

- Includes six FMC line cards with dual QSFP-DD interfaces.

Description

Raptor II is a system level development platform that enables the rapid development of switching systems utilizing Praesum DQU switching IP. Based on a leading edge Stratix* 10 MX FPGA, it supports the customization of switching algorithms and integration of specialized packet processing IP within the DQU framework.

Targeted at leading edge wireline, wireless infrastructure, and Mil-Aero applications, the Raptor II platform can accelerate platform development by allowing wirespeed evaluation of algorithms in a live network.
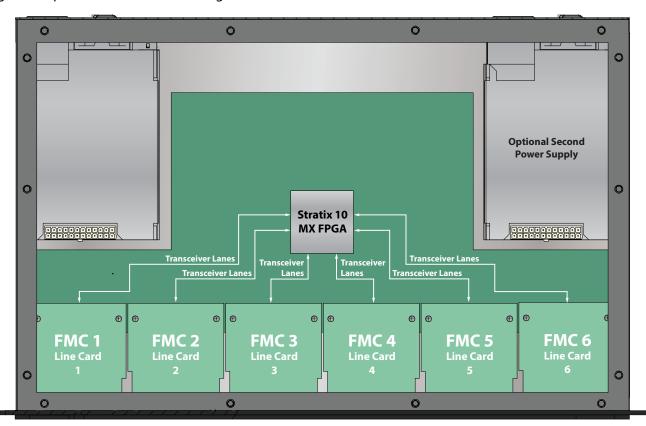
Figure 1 *Raptor II* RapidIO 4.x Switching Development Platform Product Image

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Figure 1 *Raptor II* Functional Block Diagram